

TOUCH PANEL  
**SPECIFICATION**

<b>Model:</b>	CTP-UE055FH-HA36-A020
<b>Version:</b>	V1.0
<b>Date:</b>	20180118

- Preliminary Specification** 样品规格书
- Final Specification** 量产规格书

**Customer Confirmation** 客户确认

Approved by	Notes

Please return one of the copies of the specification with your signature to us within two weeks after you receive this document. If it is not returned, we will assume that you agree to the entire contents of this specification document.

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**VIEWE Confirmation** 优奕确认

Prepared by	Reviewed by	Approved by



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## 1. GENERAL INFORMATION

### 1.1 Features

Item 项目	Specification 规格	Remark 备注
TP Size(触屏尺寸)	5.5	Inch(Diagonal)
Overall Dimensions (外形尺寸)	73.8(W)*150.8(H)*0.975(D)	mm
View Area(可视区)	68.64(W)*121.56(H)	mm
Haze(雾度)	≤1%	-
Light Transmittance(透光率)	≥85%	
Surface Hardness(表面硬度)	≥7H	500gf
Lens Protection(盖板涂层)	AF Coating	AF Coating or AR Coating
Touch Response Time(相应时间)	10	ms
IC Model(芯片型号)	GT5688	-
TP Structure(触屏结构)	GFF	-
Connection Model(连接方式)	ZIF	10PIN
Interface Type(接口类型)	I2C	
Contacts Quantity(触控点数)	10	Piont
Resolution(分辨率)	1080(W) *1920(H)	Pixel
Channel Number(通道数)	13*23	TX*RX
Lens Type(盖板材质)	龙迹	T=0.7mm

### 1.2 Build Stack

Item 项目	Specification 规格	Unit 单位	Remark 备注
Coverlens(盖板)	0.7	mm	First layer
Adhesive_1(黏合剂)	0.125	mm	Second layer
ITO_1(ITO基材)	0.05	mm	Third layer
Adhesive_2(黏合剂)	0.05	mm	Forth layer
ITO_2(ITO基材)	0.05	mm	Fifth layer

## 2. ELECTRICAL CHARACTERISTICS

### 2.1 Absolute Maximum Ratings

Item 项目	Symbol 符号	Min. 最小值	Max. 最大值	Unit 单位
Analog Power	AVDD	-0.3	4.2	V
Digital Power	VDDIO	-0.3	4.2	V
Operating temperature	Top	-20	70	°C
Storage temperature	Tst	-40	80	°C

(Ta=+25°C, AGND=DGND=0V)

Note1: All of the voltages listed above are with respect to DGND=AGND=0V

Note2: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above.

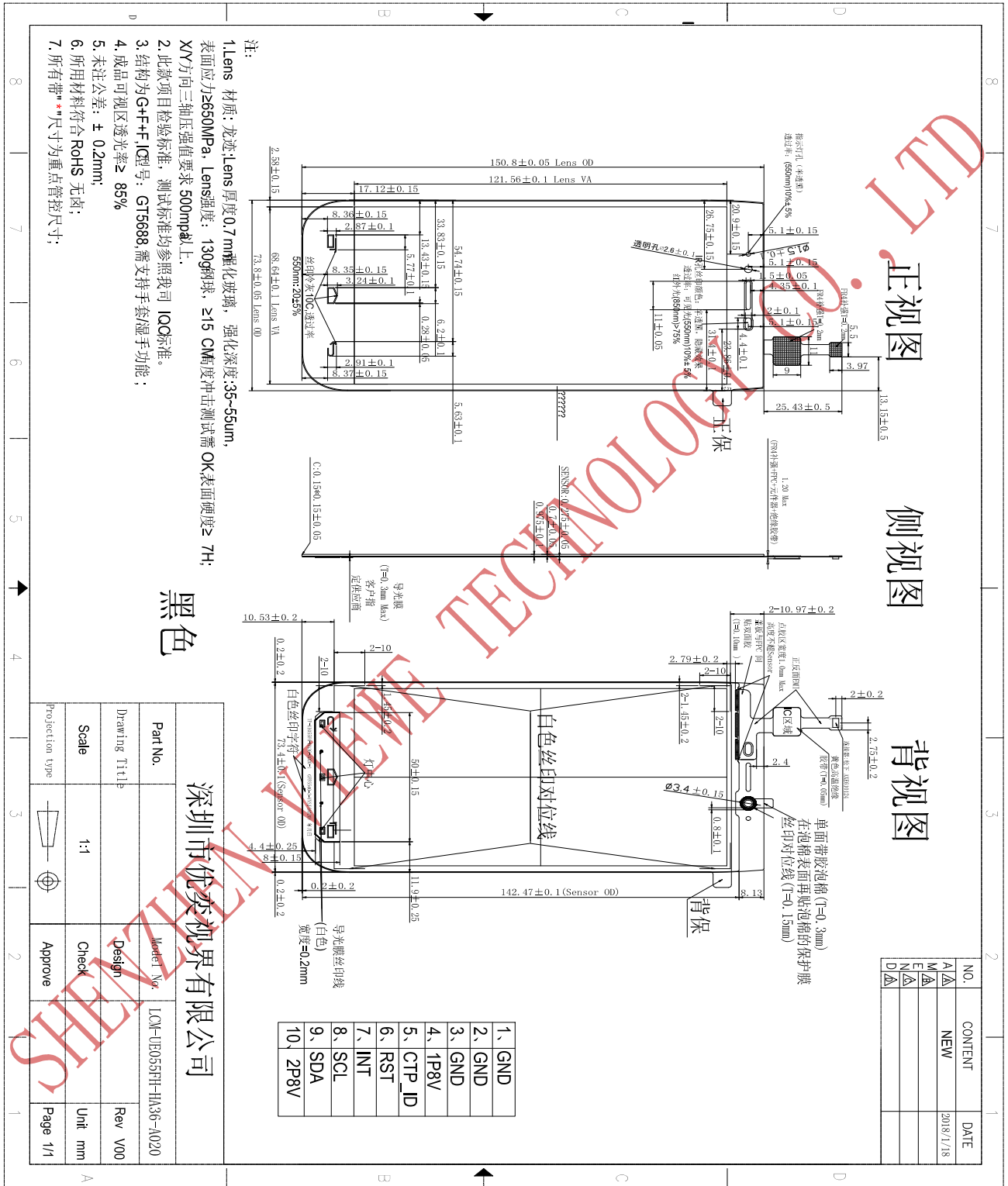
### 2.2 Recommended Operating Conditions

Item 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位
Power For Analog Voltage	AVDD	2.8	-	3.6	V
Power For Digital Voltage	VDDIO	1.7	-	3.6	V
Logic Input High Voltage	V <sub>IH</sub>	0.7VDDIO	-	VDDIO	V
Logic Input Low Voltage	V <sub>IL</sub>	0	-	0.3VDDIO	V
Module Power Consumption	P <sub>TP</sub>	-	0.056	-	Watt

(Ta=+25°C, AGND=DGND=0V)

Note: All of the voltages listed above are with respect to DGND=AGND=0V

### 3. MECHANICAL DRAWING



## 4. PIN ASSIGNMENT

Pin No. 序号	Symbol 符号	I/O	Description 描述
1	GND	P	Power Ground
2	GND	P	Power Ground
3	GND	P	Power Ground
4	1P8V	P	Power supply for logic & analog circuit
5	CTP_ID	-	Dummy
6	RESET	I	Global reset for TP, active is low
7	INT	O	Interrupt Signal
8	SCL	I	I2C Serial Clock
9	SDA	I/O	I2C Serial Data Input or Output
10	VDD	P	Power supply for logic & analog circuit

I: Input; O: Output; P: Power

## 5. RELIABILITY

Item 项目	Test Condition 测试条件	Remark 备注
High Temperature Storage	Ta =+80°C / 96Hours	Note1,2,3
Low Temperature Storage	Ta =-40°C / 96Hours	Note1,2,3
High Temperature Operating	Ta =+70°C / 96Hours	Note1,2,3
Low Temperature Operating	Ta =-20°C / 96Hours	Note1,2,3
Temperature Cycle Storage Test	-20°C/30min ↔+70°C /30min for 30cycles,Transfer time less than 5min	Note2,3
Thermal Humidity Storage Test	60°C x 90%RH / 96Hours	Note2,3
Ball Drop Test	Test ball weight:130g Test height:30cm Test position:center/3times	-
Bonding Tension Test	≥500gf/cm,Sensor face up,FPC&sensor for 90°	-
ESD	Contact: ±6KV,Air: ±10KV 150PF/330Ω,5Points/panel,5times	Note4

### Inspection after Test:

Note1: Ta is the ambient temperature of samples.

Note 2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.

Note 3: Before cosmetic and function tests , the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.